

MTNetworks

Product Specification

155M SFP 550m Transceiver Module

MTSP8501L-CDSR

Features

- Data-rate of 155M operation
- 850nm VCSEL laser and PIN photodetector
- Compliant with SFP MSA and SFF-8472 with duplex LC receptacle
- Digital Diagnostic Monitoring:
- Internal Calibration or External Calibration
- 500m transmission with 50/125 μ m MMF
- 300m transmission with 62.5/125 μ m MMF
- Compatible with RoHS
- +3.3V single power supply
- Operating case temperature:
 - Standard : 0 to +70°C
 - Extended : -20 to +85°C

Applications

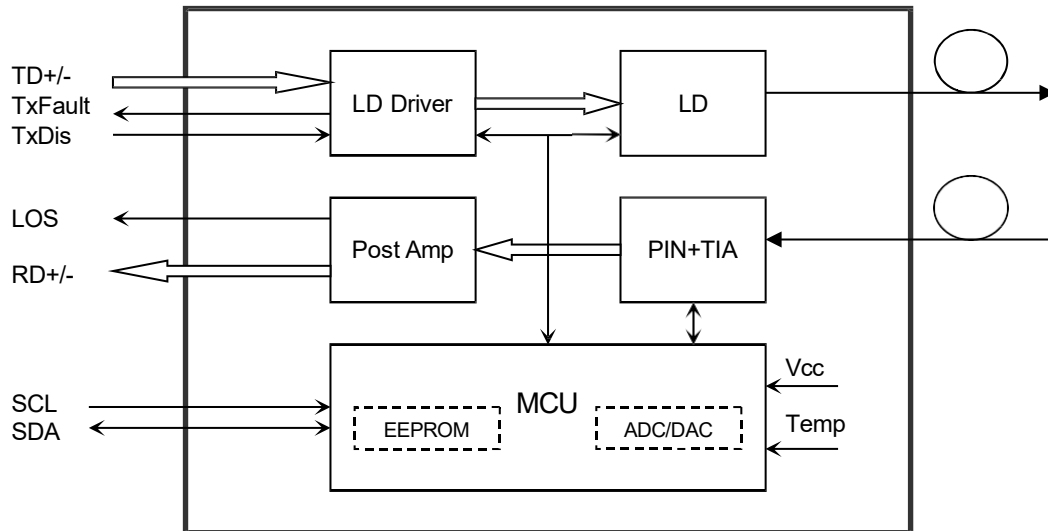
- Gigabit Ethernet
- Fiber Channel
- Switch to Switch interface
- Switched backplane applications
- Router/Server interface
- Other optical transmission systems

Descriptions

The SFP transceivers are high performance, cost effective modules supporting data-rate of 155M and 550m transmission distance with MMF.

The transceiver consists of three sections: a VCSEL laser transmitter, a PIN photodiode integrated with a trans-impedance preamplifier (TIA) and MCU control unit. All modules satisfy class I laser safety requirements.

The transceivers are compatible with SFP Multi-Source Agreement (MSA) and SFF-8472. For further information, please refer to SFP MSA.



1. Absolute Maximum Ratings

Parameter	Symbol	Min	Max	Unit	Parameter
Supply Voltage	Vcc	-0.5	4.5	V	Supply Voltage
Storage Temperature	Ts	-40	+85	°C	Storage Temperature
Operating Humidity	-	5	85	%	Operating Humidity

2. Recommended Operating Conditions

Parameter	Symbol	Min	Typical	Max	Unit
Operating Case Temperature	Standard	Tc	0	+70	°C
	Extended		-20		+85
Power Supply Voltage	Vcc	3.13	3.3	3.47	V
Power Supply Current	Icc			300	mA
Data Rate			1.25		Gbps

3. Pin Description

Pin	Signal Name	Description	Plug Seq.	Notes
1	VEET	Transmitter Ground	1	
2	TX FAULT	Transmitter Fault Indication	3	Note 1
3	TX DISABLE	Transmitter Disable	3	Note 2
4	MOD_DEF(2)	SDA Serial Data Signal	3	Note 3
5	MOD_DEF(1)	SCL Serial Clock Signal	3	Note 3
6	MOD_DEF(0)	TTL Low	3	Note 3
7	Rate Select	Not Connected	3	
8	LOS	Loss of Signal	3	Note 4
9	VEER	Receiver ground	1	
10	VEER	Receiver ground	1	
11	VEER	Receiver ground	1	
12	RD-	Inv. Received Data Out	3	Note 5
13	RD+	Received Data Out	3	Note 5
14	VEER	Receiver ground	1	
15	VCCR	Receiver Power Supply	2	
16	VCCT	Transmitter Power Supply	2	
17	VEET	Transmitter Ground	1	
18	TD+	Transmit Data In	3	Note 6
19	TD-	Inv. Transmit Data In	3	Note 6
20	VEET	Transmitter Ground	1	

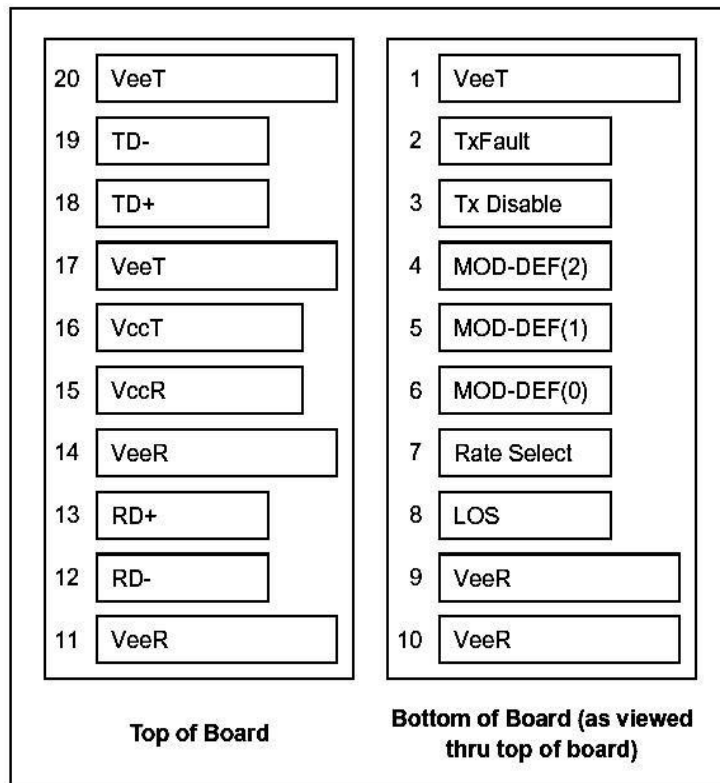
Notes:

Plug Seq.: Pin engagement sequence during hot plugging.

1. TX Fault is an open collector output, which should be pulled up with a 4.7k~10kΩ resistor on the host board to a voltage between 2.0V and Vcc+0.3V. Logic 0 indicates normal operation; Logic 1 indicates a laser fault of some kind. In the low state, the output will be pulled to less than 0.8V.
2. TX Disable is an input that is used to shut down the transmitter optical output. It is pulled up within the module with a 4.7k~10kΩ resistor. Its states are:

Low (0 to 0.8V): Transmitter on
 (>0.8V, < 2.0V): Undefined
 High (2.0 to 3.465V): Transmitter Disabled
 Open: Transmitter Disabled

3. Mod-Def 0,1,2. These are the module definition pins. They should be pulled up with a 4.7k~10kΩ resistor on the host board. The pull-up voltage shall be VccT or VccR.
 Mod-Def 0 is grounded by the module to indicate that the module is present
 Mod-Def 1 is the clock line of two wire serial interface for serial ID
 Mod-Def 2 is the data line of two wire serial interface for serial ID
4. LOS is an open collector output, which should be pulled up with a 4.7k~10kΩ resistor. Pull up voltage between 2.0V and Vcc+0.3V. Logic 1 indicates loss of signal; Logic 0 indicates normal operation. In the low state, the output will be pulled to less than 0.8V.
5. RD-/+ : These are the differential receiver outputs. They are internally AC-coupled 100 differential lines which should be terminated with 100Ω (differential) at the user SERDES.
6. TD-/+ : These are the differential transmitter inputs. They are internally AC-coupled, differential lines with 100Ω differential termination inside the module.



PIN Definitions

4. Optical and Electrical Characteristics

Parameter	Symbol	Min	Typ	Max	Unit	Ref.
Transmitter						
Centre Wavelength	λ_c	830	850	860	nm	
Spectral Width (RMS)	$\Delta\lambda$			0.85	nm	
Average Output Power	P _{out}	-9.5		-3	dBm	1
Extinction Ratio	ER	9			dB	
Optical Rise/Fall Time (20%~80%)	tr/tf			0.26	ns	
Data Input Swing Differential	V _{IN}	400		1800	mV	2
Input Differential Impedance	Z _{IN}	90	100	110	Ω	
TX Disable	Disable		2.0		V _{cc}	V
	Enable		0		0.8	V
TX Fault	Fault		2.0		V _{cc}	V
	Normal		0		0.8	V
Receiver						
Centre Wavelength	λ_c	770		860	nm	
Receiver Sensitivity				-18	dBm	3
Receiver Overload		-3			dBm	3
LOS De-Assert	LOSD			-20	dBm	
LOS Assert	LOSA	-30			dBm	
LOS Hysteresis		1		4	dB	
LOS	High	2.0		V _{cc}	V	
	Low			0.8	V	

Notes:

5. The optical power is launched into MMF.
6. PECL input, internally AC-coupled and terminated.
7. Measured with a PRBS 2⁷-1 test pattern @1250Mbps, BER $\leq 1 \times 10^{-12}$.
8. Internally AC-coupled.

4. Timing and Electrical

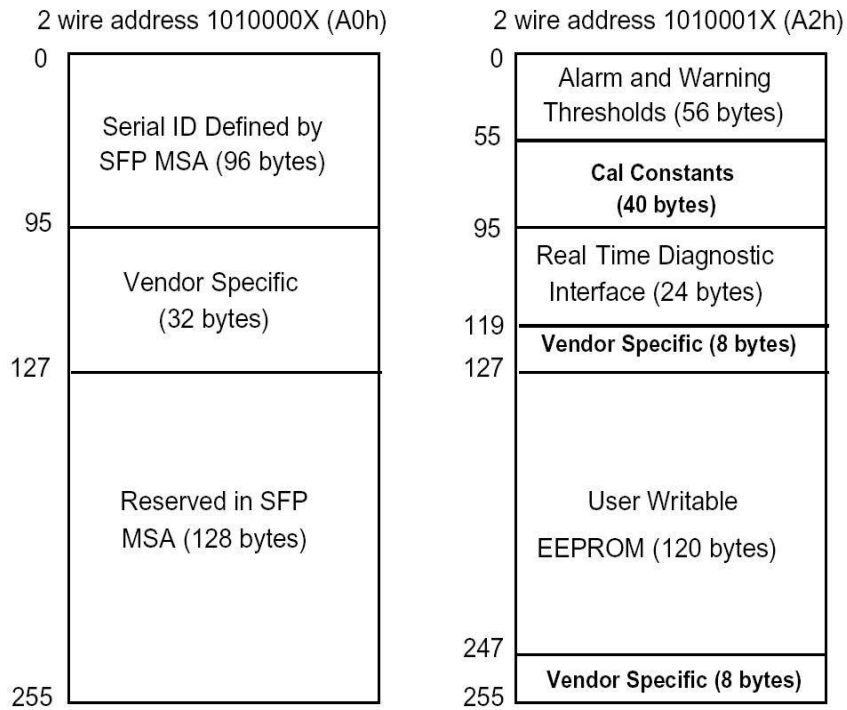
Parameter	Symbol	Min	Typ	Max	Unit	Notes
Tx Disable Negate Time	t_on			1	ms	1
Tx Disable Assert Time	t_off			10	μs	
Time To Initialize, including Reset of Tx Fault	t_init			300	ms	2
Tx Fault Assert Time	t_fault			100	μs	3
Tx Disable To Reset	t_reset	10			μs	
LOS Assert Time	t_loss_on			100	μs	
LOS De-assert Time	t_loss_off			100	μs	4
Serial ID Clock Rate	f_serial_clock			400	KHz	
MOD_DEF (0:2)-High	V _H	2		V _{cc}	V	
MOD_DEF (0:2)-Low	V _L			0.8	V	

5. Diagnostics

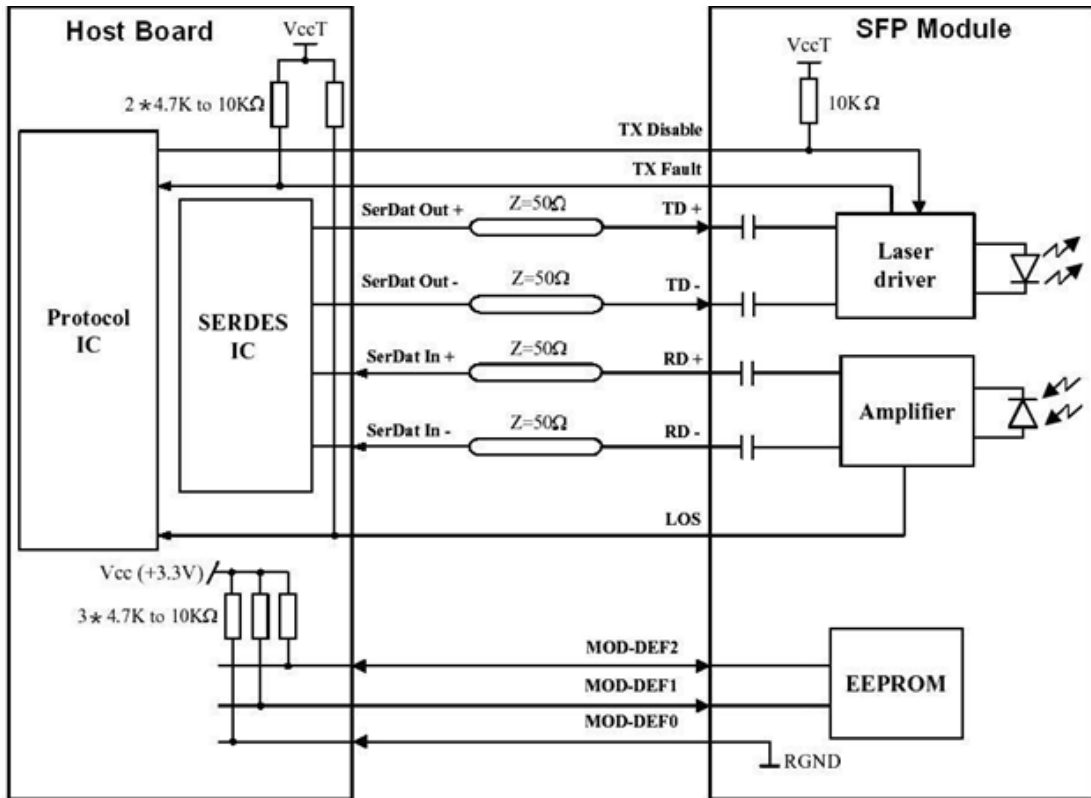
Parameter	Range	Unit	Accuracy	Calibration
Temperature	0 to +70	°C	±3°C	Internal / External
	-20 to +85			
Voltage	3.0 to 3.6	V	±3%	Internal / External
Bias Current	0 to 100	mA	±10%	Internal / External
TX Power	-9.5 to -3	dBm	±3dB	Internal / External
RX Power	-22 to -3	dBm	±3dB	Internal / External

6. Digital Diagnostic Memory Map

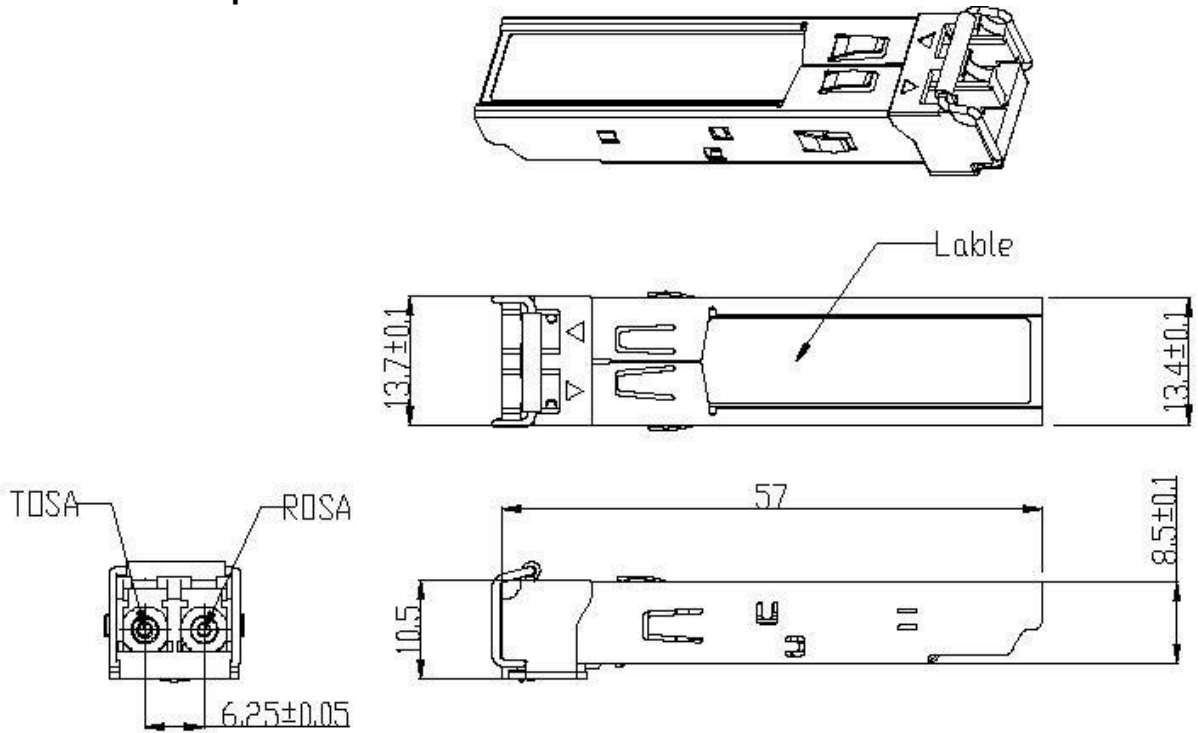
The transceivers provide serial ID memory contents and diagnostic information about the present operating conditions by the 2-wire serial interface (SCL, SDA). The diagnostic information with internal calibration or external calibration all are implemented, including received power monitoring, transmitted power monitoring, bias current monitoring, supply voltage monitoring and temperature monitoring. The digital diagnostic memory map specific data field defines as following.



7. Recommended Interface Circuit



8. Mechanical Specification



9. Ordering information

Part Number	Product Description
MTSP8501L-CDSR	155M SFP 850nm 550m Transceiver Module

10. Revision History

Revision	Date	Description
Rev A0	01/03/2019	initial release

11. For More Information

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